



XAVIER INSTITUTE OF ENGINEERING

Mahim, Mumbai 400016

Department of Information Technology

(NBA Accredited)

(Approved by AICTE, Govt. of Maharashtra and Affiliated to University of Mumbai)

Class/ Sem/ A.Y: SE IT/ IV/ 2022-23

Date: 20/03/2023

NOTICE

All students of SE-IT are informed that **Poster Making Competition on Computer Organization Architecture and Microprocessors** is scheduled on **31st March 2023 at 1:30 pm**. Top three groups will be awarded with prize of Rs. 500/ each.

Note: Attendance is compulsory.

Ms. Meena Ugale

(Subject in-charge)



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Title of the event: Poster Making Competition

Event Date / Time: 31st March 2023 at 1:30 pm

Event Coordinators: Ms. Meena Ugale

Details of Resource Person: -

No. of Participants: 67

Class/Semester: SE/IV

Objectives:

The objective behind organization of poster making competition is to bring out the creative expression of students and to gauge their knowledge and awareness of various trends in Computer Organization & Architecture.

The aim of the Poster Making Competition is not just to judge the students based on their creative aspects but also to trigger thought process among the students towards recent trends in the field of Computer Organization & Architecture.

Description:

The poster making competition is organized on 31st March 2023. The students formed a group of 3 to 4 and selected the topics. They prepared A3 size poster and presented it.

Judgement criteria was based on Poster content, Poster Organization and Delivery of the content.

Total 18 groups participated in this event and top three groups awarded prize of Rs. 500/ each.

Group No.	Roll No.	Name	Topic
1	2	Aditiya Nalawade	RISC & CISC
	26	Khushal Jogi	
	33	Piyush Mane	
	35	Ajay Pandey	
2	3	Aniket Badgujar	A11 Bionic Processor
	25	Saket Jha	
	32	Ajay Kurien	
3	9	Jovan Creado	Architecture of ARM Processor
	11	Rajaram Desai	

	14	Vedant Gharat	
	28	Jayesh Kakade	
4	36	Mangesh Pangam	ARM7
	40	Saish Rane	
5	43	Rakshita Sarap	Intel Architecture Processors Pipeline
	49	Harsh Sharma	
	53	Sumedh Shinde	
	61	Sumit Vishwakarma	
6	64	Belona Cerejo	How Processors are manufactured?
	70	Nelson Kolas	
	67	Sanuel Pallikonda	
7	68	Brice Pimenta	Memory Organization
	23	Rohan Jaiswal	
	24	Yash jaiswar	
	27	Ronak Joshi	
8	34	Vedang Nijap	Dedicated vs Integrated Graphics Cards
	44	Aditi Satam	
	51	Aditiya Shinde	
	55	Pratham Singh	
9	56	Shreya Singh	Microprocessors Security & Encryption Techniques
	69	Khizar Shaikh	
	65	Gaurav Kashelkar	
10	66	Siddhi Oswal	RAID
	46	Fradeen Shaikh	
	47	Anas Shaikh	
11	48	Mohtahsim Shaikh	Embedded Systems
	29	Riya Kamble	
	37	Shruti Parade	
	39	Lekha Pulavarthy	
12	42	Anshu Sakhare	Intel vs AMD processors
	8	Parth Chaudhary	
	16	Harshvardhan Gupta	
	19	Sejal Gupta	
13	22	Harshit Jain	Quantam Computing
	3	Siddhi Awlegaonkar	
	13	Aditya Ghadge	
	21	Shreya Jadhav	
14	63	Sacchidanand Yadav	Future trends in Bus Technology
	45	Akib Sayed	
	50	Shubham Sharma	
	59	Ishan Vaghela	
15	62	Neha Yadav	History of Intel Processors
4	Mayuresh Balsaraf		

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Date	Description	Debit	Credit



	41	Shivam Sahu	Instruction set of Pentium Processor	
	57	Vivek Singh		
	60	Prathamesh Vaidya		
16	6	Vedant Chaudhari		
	10	Rupesh Darpe		
	12	Divyajothi Raja		
	15	Salil Gujar		
17	18	Saksham Gupta		Havard Architecture
	7	Saahil Chaurasia		
	17	Pratik Gupta		
	20	Steve Hetya		
18	38	Piyush Singh		Assemblers
	52	Siddharth Shinde		
	54	Kunal Singh		
	58	Gauresh Somwanshi		

The winners of this competition are as follows.

Group No.	Roll No.	Name of Student	Topic
11	29	Riya Kamble	Embedded Systems
	37	Shruti Parade	
	39	Lekha Pulavarthy	
	42	Anshu Sakhare	
15	4	Mayuresh Balsaraf	History of Intel Processors
	41	Shivam Sahu	
	57	Vivek Singh	
	60	Prathamesh Vaidya	
16	6	Vedant Chaudhari	Instruction set of Pentium Processor
	10	Rupesh Darpe	
	12	Divyajothi Raja	
	15	Salil Gujar	

COs / LOs achieved (if any): -

POs achieved: PO1, PO2, PO8, PO9, PO10, PO12

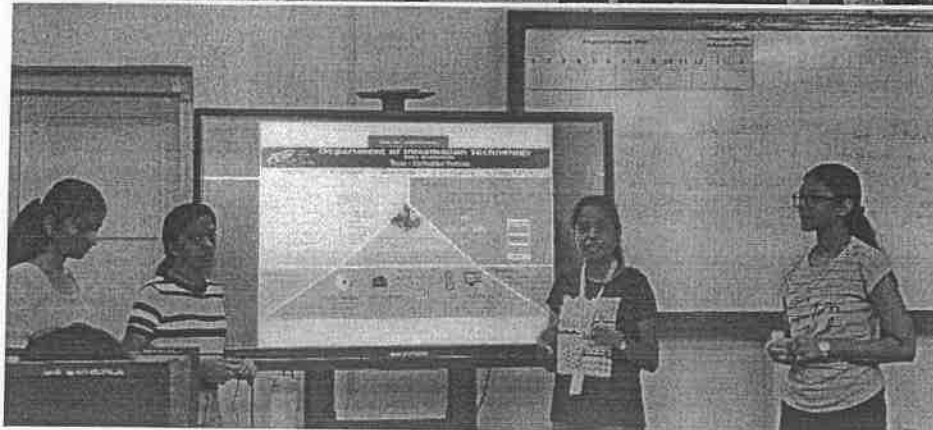
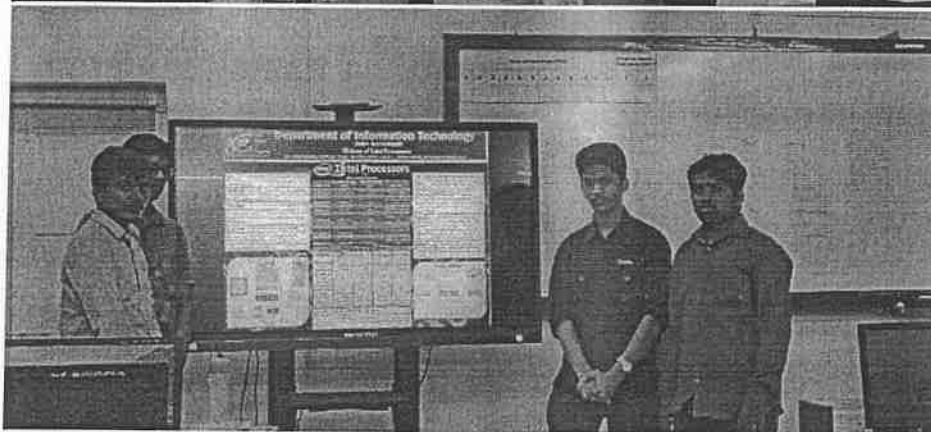
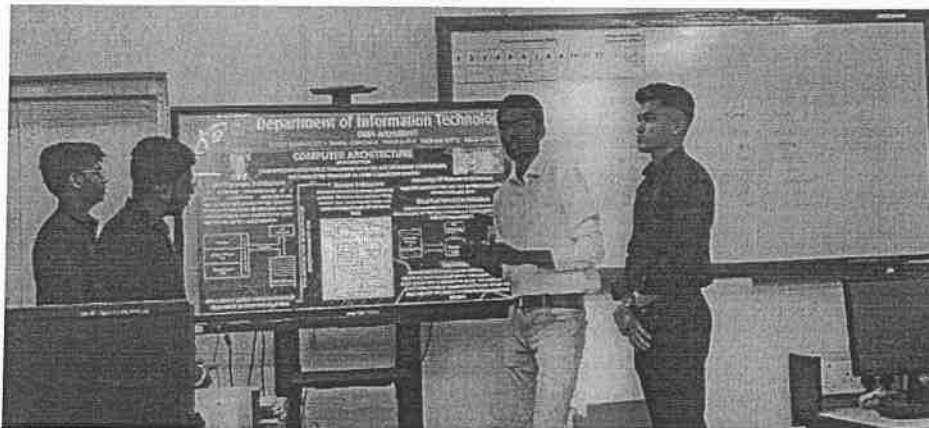
PSOs achieved: PSO1, PSO2

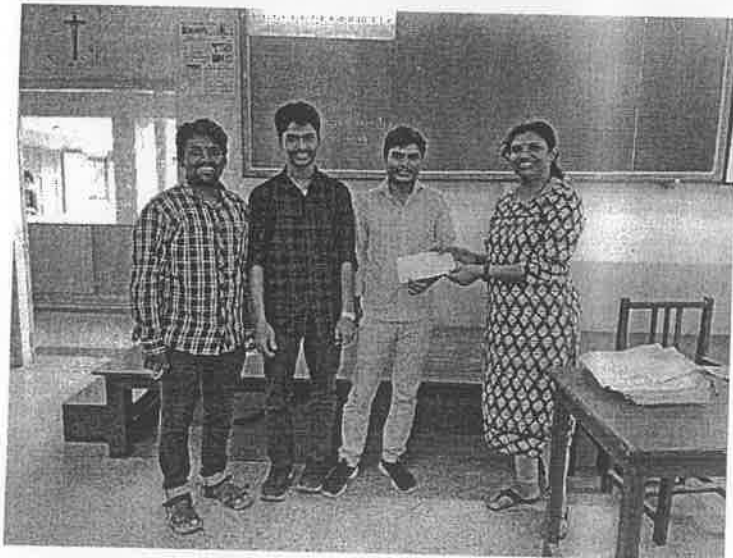
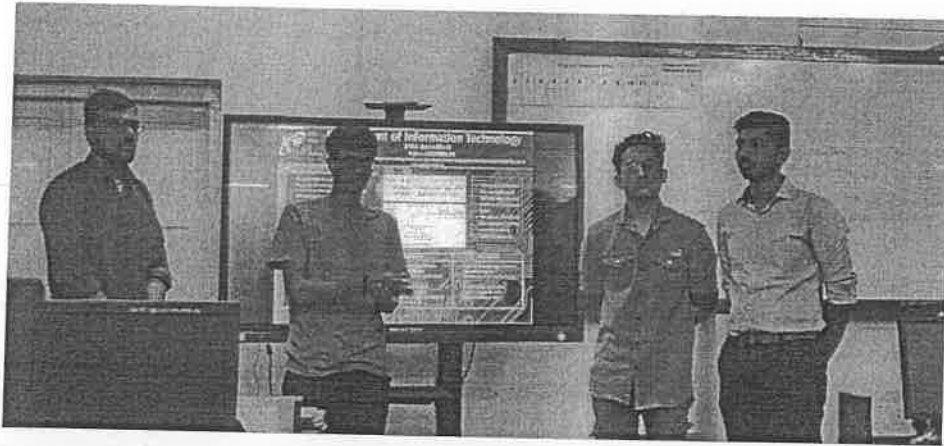



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Event Photos:






Ms. Meena Ugale
Event Coordinator


Dr. Jaychandra Upadhyay
HOD-IT


Dr. Y.D. Venkatesh
Principal

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Poster Making Competition

Subject: Computer Organization & Architecture

Class: SE

SEM: IV

AY:2022-23

Group No.	Roll No.	Name	Topic	Sign
1	2	Aditiya Nalawade	RISC & CISC	Aditiya
	26	Khushal Jogi		Khushal
	33	Piyush Mane		Piyush
	35	Ajay Pandey		Ajay
2	3	Aniket Badgujar	A11 Bionic Processor	Aniket
	25	Saket Jha		Saket
	32	Ajay Kurien		Ajay
3	9	Jovan Creado	Architecture of ARM Processor	Jovan
	11	Rajaram Desai		Rajaram
	14	Vedant Gharat		Vedant
	28	Jayesh Kakade		Jayesh
4	36	Mangesh Pangam	ARM7	Mangesh
	40	Saish Rane		Saish
	43	Rakshita Sarap		Rakshita
5	49	Harsh Sharma	Intel Architecure Processors Pipeline	Harsh
	53	Sumedh Shinde		Sumedh
	61	Sumit Vishwakarma		Sumit
	64	Belona Cerejo		Belona
6	70	Nelson Kolas	How Processors are manufactured?	Nelson
	67	Sanuel Pallikonda		Sanuel
7	68	Brice Pimenta	Memory Organization	Brice
	23	Rohan Jaiswal		Rohan
	24	Yash jaiswar		Yash
	27	Ronak Joshi		Ronak
8	34	Vedang Nijap	Dedicated vs Integrated Graphics Cards	Vedang
	44	Aditi Satam		Aditi
	51	Aditiya Shinde		Aditiya
	55	Pratham Singh		Pratham
9	56	Shreya Singh	Microprocessors Security & Encryption Techniques	Shreya
	69	Khizar Shaikh		Khizar
	65	Gaurav Kashelkar		Gaurav
10	66	Siddhi Oswal	RAID	Siddhi
	46	Fradeen Shaikh		Fradeen
	47	Anas Shaikh		Anas
11	48	Mohtahsim Shaikh	Embedded Systems	Mohtahsim
	29	Riya Kamble		Riya
	37	Shruti Parade		Shruti
	39	Lekha Pulavarthy		Lekha

12	42	Anshu Sakhare	Intel vs AMD processors	<u>Akhare</u>
	8	Parth Chaudhary		<u>Chaudhary</u>
	16	Harshvardhan Gupta		<u>Harsh</u>
	19	Sejal Gupta		<u>Gupta</u>
13	22	Harshit Jain	Quantam Computing	<u>Harshit</u>
	3	Siddhi Awlegaonkar		<u>Siddhi</u>
	13	Aditya Ghadge		<u>Aghadge</u>
	21	Shreya Jadhav		<u>Shreya</u>
14	63	Sacchidanand Yadav	Future trends in Bus Technology	<u>Sacchi</u>
	45	Akib Sayed		<u>Akib</u>
	50	Shubham Sharma		<u>Shubham</u>
	59	Ishan Vaghela		<u>Ishan</u>
15	62	Neha Yadav	History of Intel Processors	<u>Neha</u>
	4	Mayuresh Balsaraf		<u>Balsaraf</u>
	41	Shivam Sahu		<u>Shivam</u>
	57	Vivek Singh		<u>Vivek</u>
16	60	Prathamesh Vaidya	Instruction set of Pentium Processor	<u>P.A. Vaidya</u>
	6	Vedant Chaudhari		<u>Vedant</u>
	10	Rupesh Darpe		<u>Rupesh</u>
	12	Divyajothi Raja		<u>Divya</u>
17	15	Salil Gujar	Havard Architecture	<u>Salil</u>
	18	Saksham Gupta		<u>Saksham</u>
	7	Saahil Chaurasia		<u>Saahil</u>
	17	Pratik Gupta		<u>Pratik</u>
18	20	Steve Hetya	Assemblers	<u>Steve</u>
	38	Piyush Singh		<u>Piyush</u>
	52	Siddharth Shinde		<u>Siddharth</u>
	54	Kunal Singh		<u>Kunal</u>
	58	Gauresh Somwanshi		<u>Gauresh</u>

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Area	Sub-Area	Notes
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